

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shigeru ONOYA
Serial No. : Not yet assigned
Filed : February 8, 2001
Title : SEMICONDUCTOR DEVICE AND METHOD OF DRIVING
SEMICONDUCTOR DEVICE

Art Unit : Unknown
Examiner : Unknown

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

5. (Amended) A method of driving a semiconductor display device according to claim 1, [any of claims 1 to 4,] wherein pixel electrodes to which the display signals having positive polarity are inputted, and pixel electrodes to which the display signals having negative polarity are inputted may differ randomly every frame period.

6. (Amended) A method of driving a semiconductor display device according to claim 1, [any of claims 1 to 4,] wherein the polarity of the display signals inputted to all of the pixel electrodes may be inverted in two adjacent frame periods.

11. (Amended) A semiconductor display device according to [any of claims 7 to 10,] claim 7, wherein the polarity data [is] comprises information regarding the polarity of the display signals inputted to all of the pixels.

Please add claims 15-23 as follows:

--15. A method of driving a semiconductor display device according to claim 2, wherein pixel electrodes to which the display signals having positive polarity are inputted, and pixel electrodes to which the display signals having negative polarity are inputted may differ randomly every frame period.--

--16. A method of driving a semiconductor display device according to claim 3, wherein pixel electrodes to which the display signals having positive polarity are inputted, and pixel electrodes to which the display signals having negative polarity are inputted may differ randomly every frame period.--

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--17. A method of driving a semiconductor display device according to claim 4, wherein pixel electrodes to which the display signals having positive polarity are inputted, and pixel electrodes to which the display signals having negative polarity are inputted may differ randomly every frame period.--

--18. A method of driving a semiconductor display device according to claim 2, wherein the polarity of the display signals inputted to all of the pixel electrodes may be inverted in two adjacent frame periods.--

--19. A method of driving a semiconductor display device according to claim 3, wherein the polarity of the display signals inputted to all of the pixel electrodes may be inverted in two adjacent frame periods.--

--20. A method of driving a semiconductor display device according to claim 4, wherein the polarity of the display signals inputted to all of the pixel electrodes may be inverted in two adjacent frame periods.--

--21. A semiconductor display device according to claim 8, wherein the polarity data comprises information regarding the polarity of the display signals inputted to all of the pixels.--

--22. A semiconductor display device according to claim 9, wherein the polarity data comprises information regarding the polarity of the display signals inputted to all of the pixels.--

--23. A semiconductor display device according to claim 10, wherein the polarity data comprises information regarding the polarity of the display signals inputted to all of the pixels.--

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Page : 3

Attorney's Docket No.: 12732-013001 / US4610

REMARKS

Claims 1-23 are pending in this application with claims 1-4, 7-10, and 12-14 being independent. Claims 5, 6, and 11 have been amended and claims 15-23 have been added. The above amendments have been made to place the application in better condition for initial examination. Support for new claims 15-23 is found in claims 5, 6, and 11 as filed. No new matter has been added. The examiner is invited to contact the undersigned with any questions at the number set forth below. Please apply any charges of credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: _____

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